PERSONAL INFORMATION	**Biase Celano**

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✉ biase.celano@gmail.com

💬 Skype: biase.celano

Sex Male | Date of birth 18/02/1969 | Nationality Italian

APPLIED FOR	**NA**

Job-related skills

In the last fifteen years I designed and participated in the design of more than 250 electronics cards, mainly microcontroller cards, analog and digital front-ends, high precision transducers, and some of them have an average yearly production that exceeded 20K pieces.

As a founder and partner of various companies I worked on:
▪ Industrial mechanical design and design of plastic injection mold.
▪ Optical shape recognition in consumer scales.
▪ Design of wireless wearable instruments for measure blood saturation and heart rate.

As a consultant and/or employer I worked on:
▪ Design of Electrocardiograph Analog Front Ends.
▪ Boards qualifications and certifications (CE, EMC, Ministry of Finances approvals) in cooperation with several Italian Certification Laboratory (www.teslab.it, www.giordano.it, www.mcssrl.net, www.dieti.unina.it and more).
▪ CERN Fast Digital Integrator v5 Analog Front End designer, Schematic revision and Printed Circuit Board design and prototype in collaboration with Università del Sannio.
▪ Electronics load cells transducers and converters for metrological applications (mainly in scales).
▪ Real-time hardware and firmware design for thermal printer controllers.
▪ Automatic in-line tests for Electronics Cash Register mass production line (Optical Character. Recognition and fingers key-press simulator mainly based on LabView).
▪ Good mastering of quality control processes (4 years as responsible for quality audit in R&D).

WORK EXPERIENCE	

January 2018 – present	**R&D Manager and hardware designer**

K-city Srl. (www.k-city.eu)

▪ Design of radio connected electronic devices.

Business or sector: Smart cities.

September 2019 – April 2020	**Research Fellow**

Istituto Nazionale di Fisica Nucleare. (www.infn.it)

▪ Darkside collaboration

Business or sector: Nuclear Research.

June 2017 – June 2018	**Research Fellow**

Istituto Nazionale di Fisica Nucleare. (www.infn.it)

▪ Monitoring the magnetic axis misalignment in axially-symmetric magnets

Business or sector: Nuclear Research.

June 2018 – June 2019	**Scholarship**

Facoltà di Medicina Veterinaria - Napoli. (www.unina.it)

▪ Monitoraggio parassitosi animali

Business or sector: Medical research.

January 2008 - present	**CEO and hardware designer**

Officina Elettronica SPA s.r.l. (www.officinaelettronica.it)

▪ Design of industrial electronics cards for Pump Station automation and embedded devices.
▪ Research and development of electronics cards for satellite tracking in military applications.
▪ Recently involved in cyber security design of radio connected electronics devices.

Business or sector: Design and prototyping of electronic boards.

May 2014 – March 2016	**CEO and R&D Manager**

Leonardo Ricerche s.r.l. (www.leonardoricerche.it)

▪ Management of 8 people in hardware and software development for Point of Sales automation and Scales.
▪ Involved in the design of a digital signature device for fiscal documents and financial transactions.

Business or sector: ICT R&D Partner.

February 2012 – November 2014	**Hardware & Mechanical R&D Manager**

Ditron s.r.l. (www.ditron.eu)

▪ Management of a 15 people R&D group.
▪ Mainly responsible of innovative products in the field of cash registers and electronics scales.

Business or sector: ECR and electronics scales production

October 2010 – July 2012	**Electronics designer**

CERN (home.cern)

▪ Fast Digital Integrator v5 AFE designer, Schematic revision and Printed Circuit Boards design and prototyping.

Business or sector: Nuclear Research

January 2007 – December 2007	**System Integrator**

Wavesystems s.r.l. (www.wavesystems.it)

▪ Creation of radio links and infrastructures for the diffusion of Internet access via WI-FI and WiMax.
▪ Software infrastructure for user Authentication, Authorization and Accounting (AAA) using Freeradius and Chillispot.
▪ Personalised version of OpenWRT White Russian running in Linksys WRT devices for users traffic data logging and illegal behaviours

Business or sector: Telecommunication and intelligence

November 2000 – December 2006	**Hardware R&D designer**

Ditron s.r.l. (www.ditron.eu)

▪ Design of electronics boards.
▪ Software drivers in Common Object Model technology for Automatic test system.
▪ Boards qualifications and certifications (CE, EMC, Ministerial approvals)

Business or sector: ECR and electronics scales production

June 2000 – November 2000 | **Researcher and Electronics designer**

Università degli Studi di Napoli – Federico II (http://www.international.unina.it)

▪ Design of an Electrocardiograph Analog Front End
▪ Design of a Microcontroller board to interface an Electrocardiograph to a GSM module.

Business or sector: Educational

EDUCATION AND TRAINING

Master degree in Electronics Engineering

Università degli Studi di Napoli – Federico II - Italy (http://www.international.unina.it)

▪ Electronics design
▪ Software and firmware development
▪ Biomedical electronics applications

PERSONAL SKILLS

Mother tongue | Italian

Other languages

UNDERSTANDING		SPEAKING		WRITING
Listening	Reading	Spoken interaction	Spoken production	
English B2	C1	B2	C1	C1
French A2	B1	A1	A2	A1

Levels: A1/2: Basic user - B1/2: Independent user - C1/2 Proficient user
Common European Framework of Reference for Languages

Communication skills | ▪ Good communication skills gained through my experience as hardware manager

Organisational / managerial skills | ▪ Leadership (lately responsible for a team of 15 people)

Computer skills | ▪ Altium Designer (professional use)
▪ Spaceclaim (professional use)
▪ LabView (basic use)
▪ EAGLE Cadsoft (Enterprise use)
▪ Keil uVision (professional use)
▪ LTSpice (semi-professional use)
▪ Borland C++ Builder, now Embarcadero (professional use)
▪ Matlab simulink (basic use)
▪ Microchip Mplab 8.0 (professional use)
▪ Good mastering of OpenOffice tools.
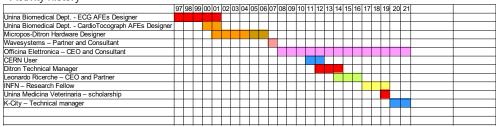▪ "Languages" used as a basic user: PHP, HTML, SQL, BASIC and more.

Other skills	▪ Carpentry

Driving licence	▪ A ▪ B

Other	

▪ Activity history

	97	98	99	00	01	02	03	04	05	06	07	08	09	10	11	12	13	14	15	16	17	18	19	20	21		
Unina Biomedical Dept. - ECG AFEs Designer	■	■	■	■	■																						
Unina Biomedical Dept. - CardioTocograph AFEs Designer					■	■																					
Micropos-Ditron Hardware Designer							■	■	■	■																	
Wavesystems – Partner and Consultant											■																
Officina Elettronica – CEO and Consultant												■	■	■	■	■	■	■	■	■	■	■	■	■	■		
CERN User															■	■											
Ditron Technical Manager																■	■	■									
Leonardo Ricerche – CEO and Partner																			■	■							
INFN – Research Fellow																				■	■	■					
Unina Medicina Veterinaria – scholarship																							■				
K-City – Technical manager																								■	■		

Unina: University of Naples Federico II
INFN: National Institute for Nuclear Physics

I hereby authorize the use of my personal data in accordance to the GDPR 679/16 - "European regulation on the protection of personal data".

Italian version only: Autorizzo il trattamento dei miei dati personali ai sensi del Dlgs 196 del 30 giugno 2003